

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

APPLICANT: Heo et al. **EXAMINER:** Angadi, Maki A
SERIAL NO: 10/734,354 **GROUP ART UNIT:** 1765
FILED: December 12, 2003 **Docket:** 8836-140 DIV (IE10718-US)
FOR: SEMICONDUCTOR DEVICE HAVING TRENCH ISOLATION
LAYER AND A METHOD OF FORMING THE SAME

Commissioner for Patents
P.O. Box 1450
Alexandria, Virginia 22313-1450

RESPONSE TO OFFICE ACTION

Sir:

This response is filed in response to the Office Action dated May 30, 2007.